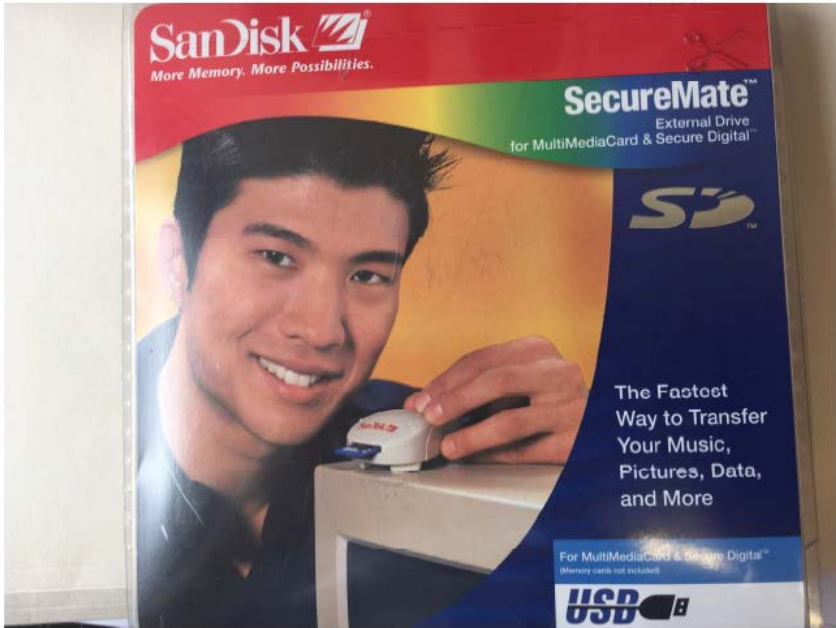



# **Exhibit G1**

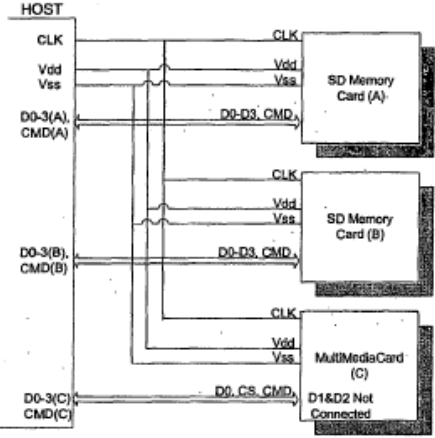
**EXHIBIT 847-34****Invalidity Claim Chart for U.S. Patent No. 7,719,847 ('847) Based Upon SanDisk SecureMate (SDDR-33)<sup>1</sup>**

<b>Claim 1</b>	<b>Anticipation by SanDisk SecureMate (SDDR-33) and/or Obviousness</b>
Apparatus comprising:	<p>SanDisk SecureMate (SDDR-33) is a reader supporting MultiMediaCard (MMC) and Secure Digital (SD) storage media.</p> <p><i>See, e.g.,</i> HP204517-HP204519.</p>  <p>HP204517</p>
a housing having a port and a surface;	SanDisk SecureMate (SDDR-33) housing has a port and a surface.

<sup>1</sup> SanDisk card reader SecureMate External Drive for MultiMediaCard & Secure Digital (SDDR-33) was offered for sale or sold to general public by SanDisk Corporation, known or used by others in the United States, and/or described in a printed publication as early as July 2001. See, for example, SDK000854.

Claim 1	Anticipation by SanDisk SecureMate (SDDR-33) and/or Obviousness
	<p><i>See, e.g.,</i> HP204517-HP204519.</p>  <p>HP204518</p>
<p>a plurality of sets of contact pins mounted on said surface at locations adapted to interface with the electrical contacts of a plurality of different type memory media cards when inserted into said port;</p>	<p>SanDisk SecureMate (SDDR-33) has contact pins to interface SD/MMC card in the same slot.</p> <p><i>See, e.g.,</i> HP204517-HP204519.</p> <p>In the alternative, a person of ordinary skill in the art at the time of the filing of the '443 Patent seeking to electrically connect to one of the memory media cards would have been motivated to seek already-available socket solutions, among which the use of a plurality of sets of contact pins in a port is commonplace, such as disclosed in a number of prior art references, including at least Admitted Prior Art (<i>See, e.g.,</i> at FIGS. 1-2), the '159</p>

Claim 1	Anticipation by SanDisk SecureMate (SDDR-33) and/or Obviousness
	<p>Patent (<i>See</i>, e.g., at FIGS 2a2 and 2b, paragraphs 15 and 35), the '044 Publication (<i>See</i>, e.g., at FIGS. 5-6), the '280 Patent (<i>See</i>, e.g., at FIGS. 5-8), '928 Publication (<i>See</i>, e.g., at FIGS. 1-5), and the '007 Patent (<i>See</i>, e.g., at FIGS. 2-3).</p>
<p>a set of signal lines connected to a controller, the number of signal lines being fewer than the number of contact pins;</p>	<p>SanDisk SecureMate (SDDR-33) includes signal lines connected to a controller.</p> <p>In the alternative, a person of ordinary skill in the art at the time of the filing of the '847 Patent would have known to use a set of signal lines connected to a controller, the number of signal lines being fewer than the number of contact pins based on, for example, the SD Specification V.1.0 and V0.96 where ground pins SD and MMC cards (pins 3 and 6) are tied directly to ground:</p> <p><b><u>SD Specification 0.96</u></b></p> <p>The SD Specification 0.96 Version discloses interfacing with the same type of cards as Knoernschild, namely MMC and SD, and discloses signal lines that are fewer than the number of contact pins with interconnections located between a controller and signal lines. <i>See</i> e.g., §3.1.</p> <p>For example, the SD Specification discloses signal lines that are fewer than the number of contact pins (the same signal line providing VSS1 and VSS2 is coupled to contact pins 3 and 6) with interconnections located between a controller and signal lines. <i>See</i> e.g., §3.1.</p> <p><b>§3.1.1 SD Bus</b></p>

Claim 1	Anticipation by SanDisk SecureMate (SDDR-33) and/or Obviousness
	 <p><b>Figure 2: SD Memory Card system bus Topology</b></p> <p>VDD, VSS1, VSS2: Power and ground signals</p> <p>In a further alternative, a person of ordinary skill in the art at the time of the alleged invention disclosed in the '847 patent was made seeking to read/write to and from a memory media card would have been motivated to seek already-available system controllers where the number of signal lines being fewer than the number of contact pins, such as disclosed in a number of prior art references, including at least the following references:</p> <p>US 6,624,063; US 6,663,007; JP 2001-184462; Intel PXA250 MMC Controller Application Note (2002); Texas Instruments TMS320VC5509 Multi Media Controller Application Report (2001); Atmel Multimedia Card Interface Datasheet (2001); Toshiba TC6374AF Controller Datasheet; Toshiba TC6371AF Controller Datasheet; SD Memory Card Specifications / Part 1. Physical Layer Specification Version 1.0; SD Memory Card Specifications / Part 1. Physical Layer Specification Version 0.96; ARM PrimeCell Multimedia Card Interface (PL180)</p>
the signal lines located between the controller and an interconnection means;	<p>SanDisk SecureMate (SDDR-33) has signal lines located between the controller and an interconnection means.</p> <p><i>See, e.g.,</i> HP204517-HP204519.</p>

Claim 1	Anticipation by SanDisk SecureMate (SDDR-33) and/or Obviousness
<p>said interconnection means being located between the signal lines and the plurality of sets of contact connecting said signal lines to said one or more contact pins; and</p>	<p>SanDisk SecureMate (SDDR-33) has interconnection means being located between the signal lines and the plurality of sets of contact connecting said signal lines to said one or more contact pins.</p> <p><i>See, e.g.,</i> HP204517-HP204519.</p>
<p>means for mapping power, ground or data signals between said signal lines and said contact pins depending upon the identification of the type of memory card inserted into said port; wherein the means for mapping comprises a controller.</p>	<p>This limitation recites a means-plus-function term for which no structure is disclosed in the specification. Accordingly, this claim is rendered invalid as indefinite under 35 U.S.C. § 112.</p> <p>To the extent that this term is not indefinite and to the extent that “mapping power, ground or data signals” is construed as communicating with a SD/MMC card in the one slot, SanDisk SecureMate (SDDR-33) has pins that communicate with a SD card or a MMC card in the same port.</p> <p><i>See, e.g.,</i> HP204517-HP204519.</p>

Claim 2	Anticipation by SanDisk SecureMate (SDDR-33) and/or Obviousness
<p>Apparatus according to claim 1 where said controller comprises means for determining the type of memory card inserted into said port.</p>	<p>This limitation recites a means-plus-function term for which no structure is disclosed in the specification. Accordingly, this claim is rendered invalid as indefinite under 35 U.S.C. § 112.</p> <p>To the extent that this term is not indefinite and to the extent “means for determining” is construed to identify SD and MMC cards, SanDisk SecureMate (SDDR-33) has a controller, able to identify the type of memory card inserted into the port.</p> <p><i>See, e.g.,</i> HP204517-HP204519.</p>

Claim 3	Anticipation by SanDisk SecureMate (SDDR-33) and/or Obviousness
Apparatus according to claim 1 wherein said interconnection means is selected from a group consisting of simple wires, flat cables, printed circuit board interconnections, or wiring traces.	<p>SanDisk SecureMate (SDDR-33) has interconnection means is selected from a group consisting of simple wires, flat cables, printed circuit board interconnections, or wiring traces.</p> <p><i>See, e.g.,</i> HP204517-HP204519.</p>